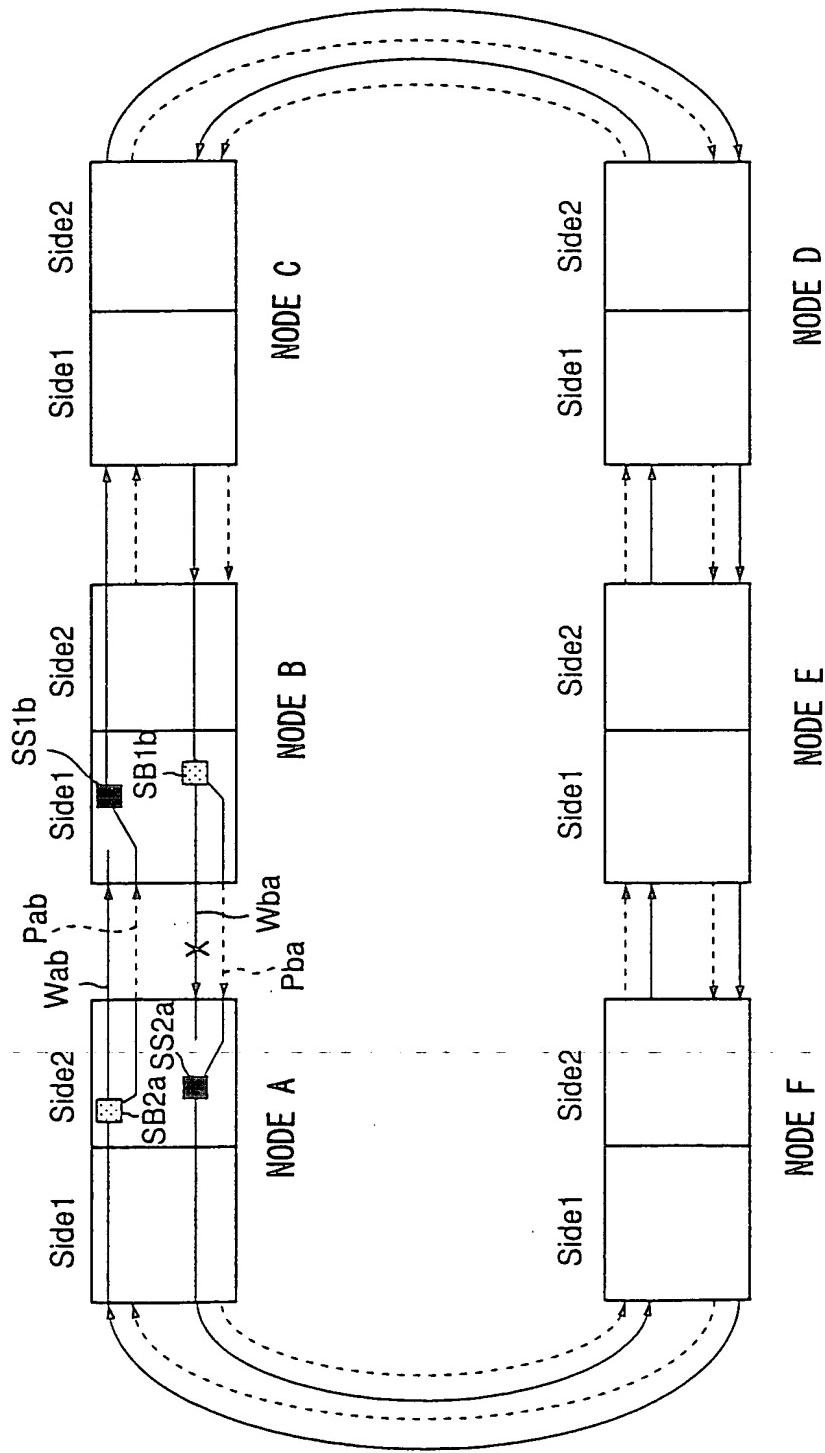


FIG. 1



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FIG. 2

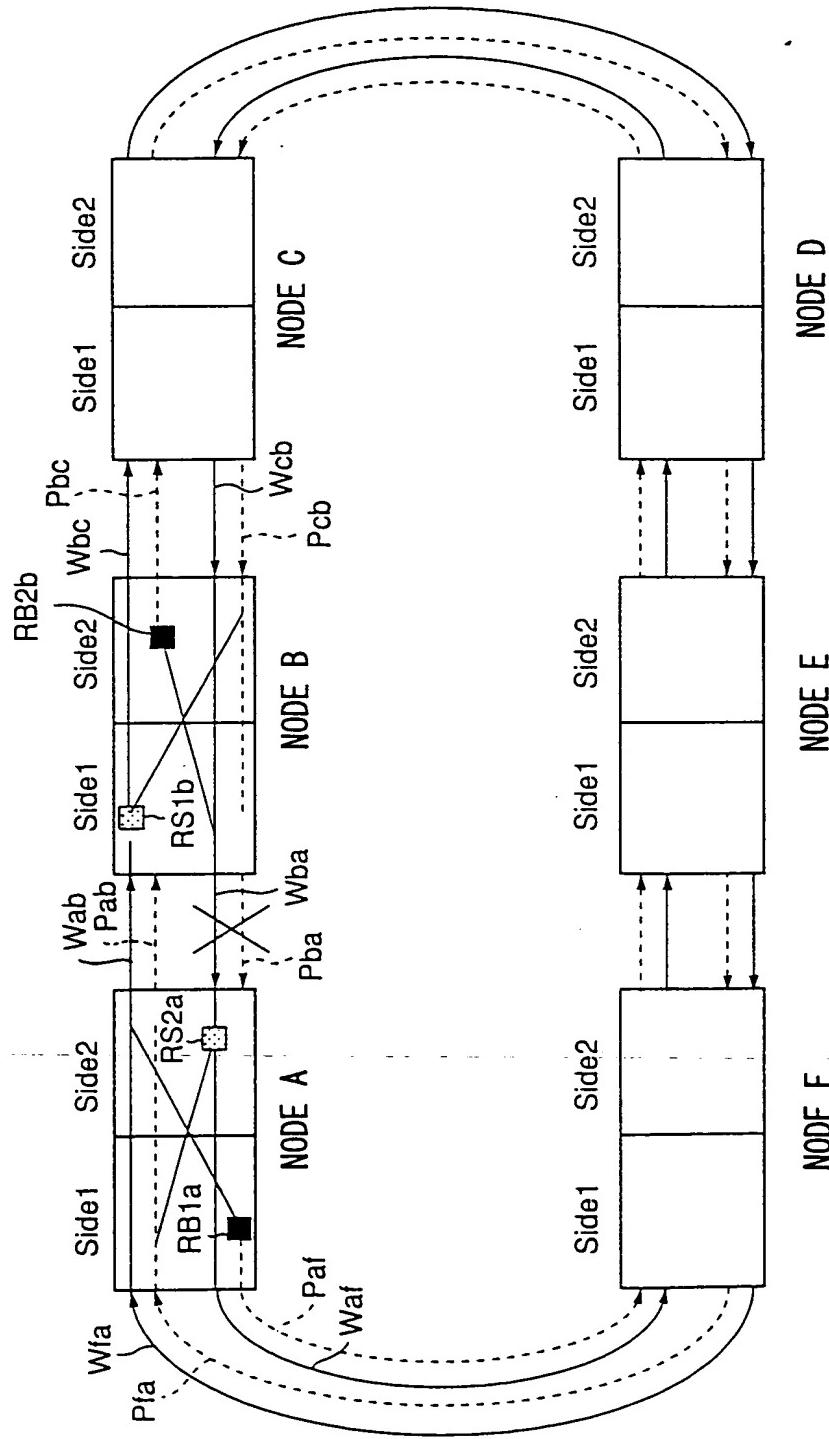
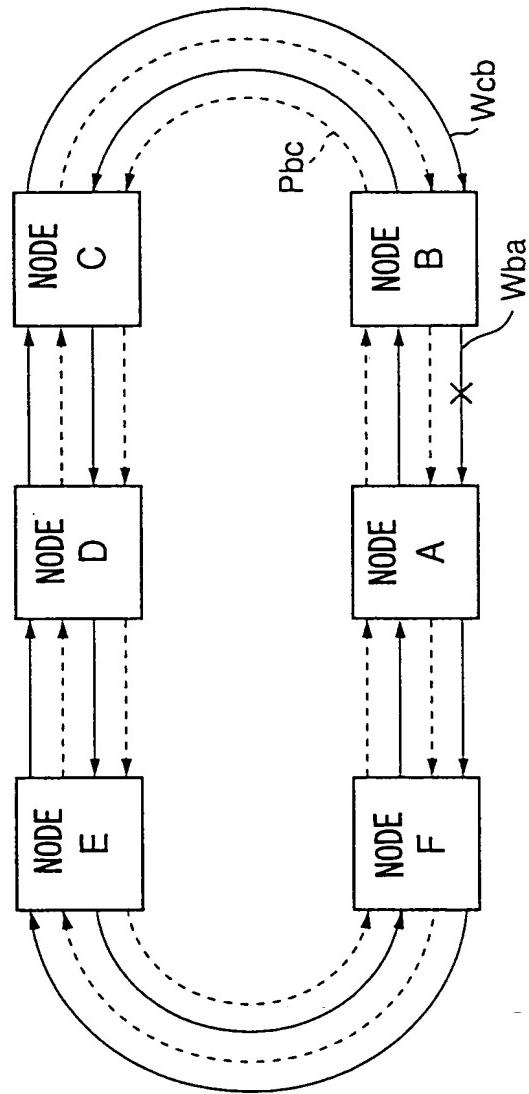


FIG. 3



0011110100000000

FIG. 4

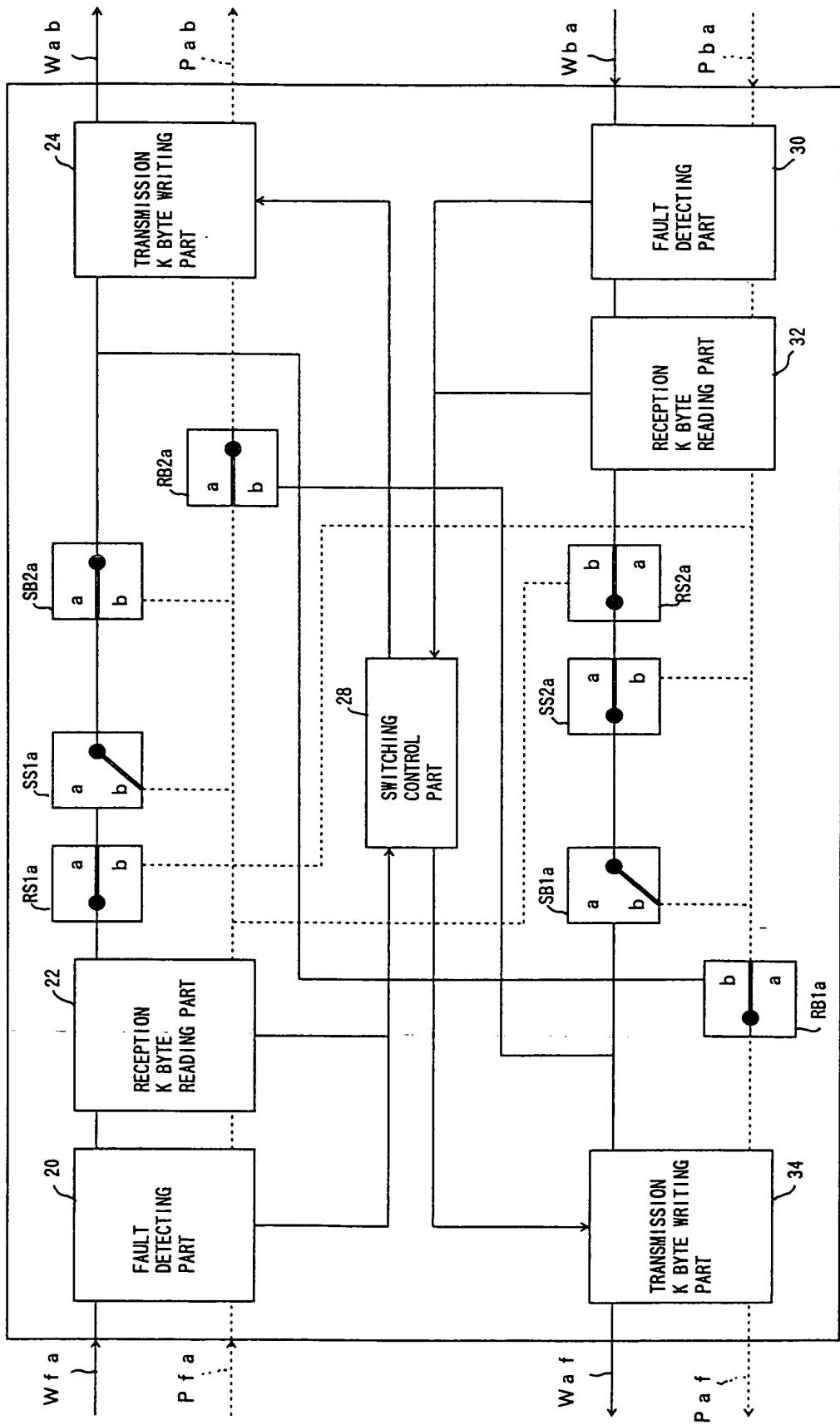
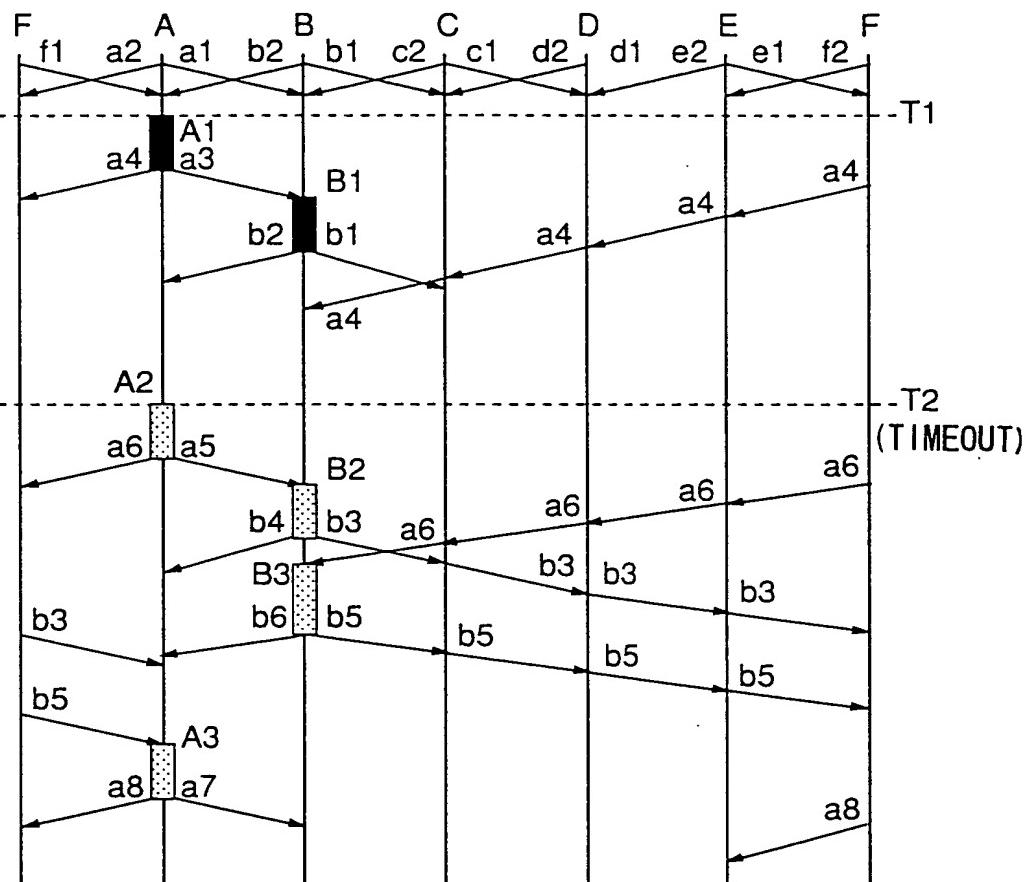


FIG. 5



**FIG. 6A**

SYMBOL	K1 BYTE		K2 BYTE		
	BIT(1-4)	BIT(5-8)	BIT(1-4)	BIT(5)	BIT(6-8)
a 1	NR	B	A	SHORT	IDLE
a 2	NR	F	A	SHORT	IDLE
b 1	NR	C	B	SHORT	IDLE
b 2	NR	A	B	SHORT	IDLE
c 1	NR	D	C	SHORT	IDLE
c 2	NR	B	C	SHORT	IDLE
d 1	NR	E	D	SHORT	IDLE
d 2	NR	C	D	SHORT	IDLE
e 1	NR	F	E	SHORT	IDLE
e 2	NR	D	E	SHORT	IDLE
f 1	NR	A	F	SHORT	IDLE
f 2	NR	E	F	SHORT	IDLE

**FIG. 6B**

a 3	S F - S	B	A	SHORT	IDLE
a 4	S F - S	B	A	LONG	IDLE
a 5	S F - R	B	A	SHORT	IDLE
a 6	S F - R	B	A	LONG	IDLE
b 3	S F - R	A	B	LONG	IDLE
b 4	R R - R	A	B	SHORT	IDLE
b 5	S F - R	A	B	LONG	Br&Sw
b 6	R R - R	A	B	SHORT	Br&Sw
a 7	S F - R	B	A	SHORT	Br&Sw
a 8	S F - R	B	A	LONG	Br&Sw

FIG. 7

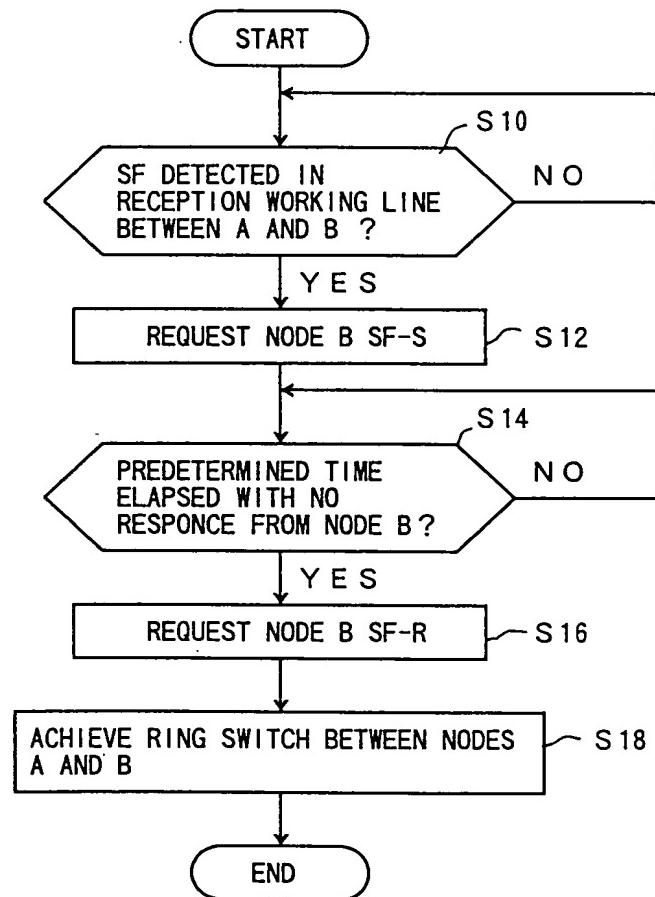


FIG. 8

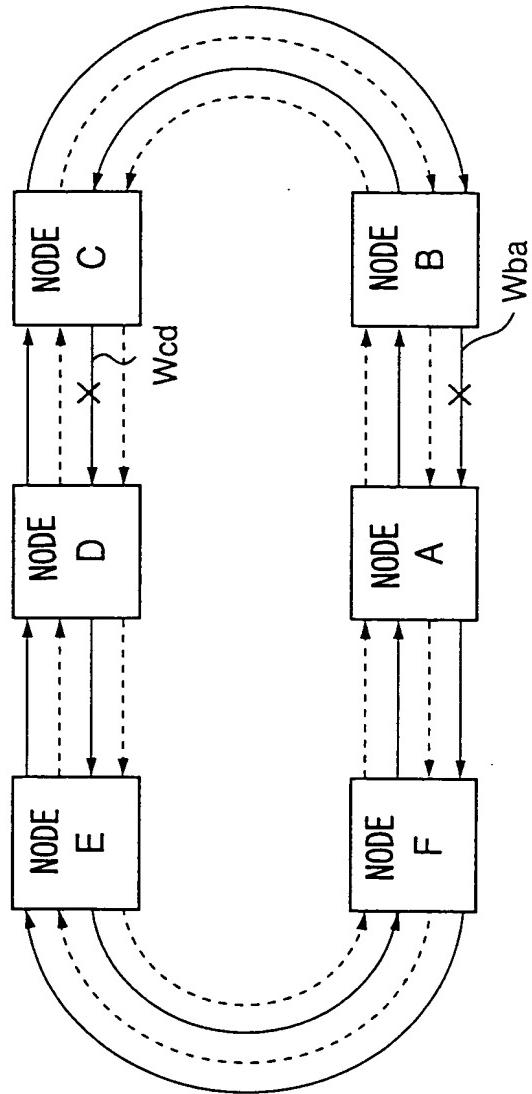
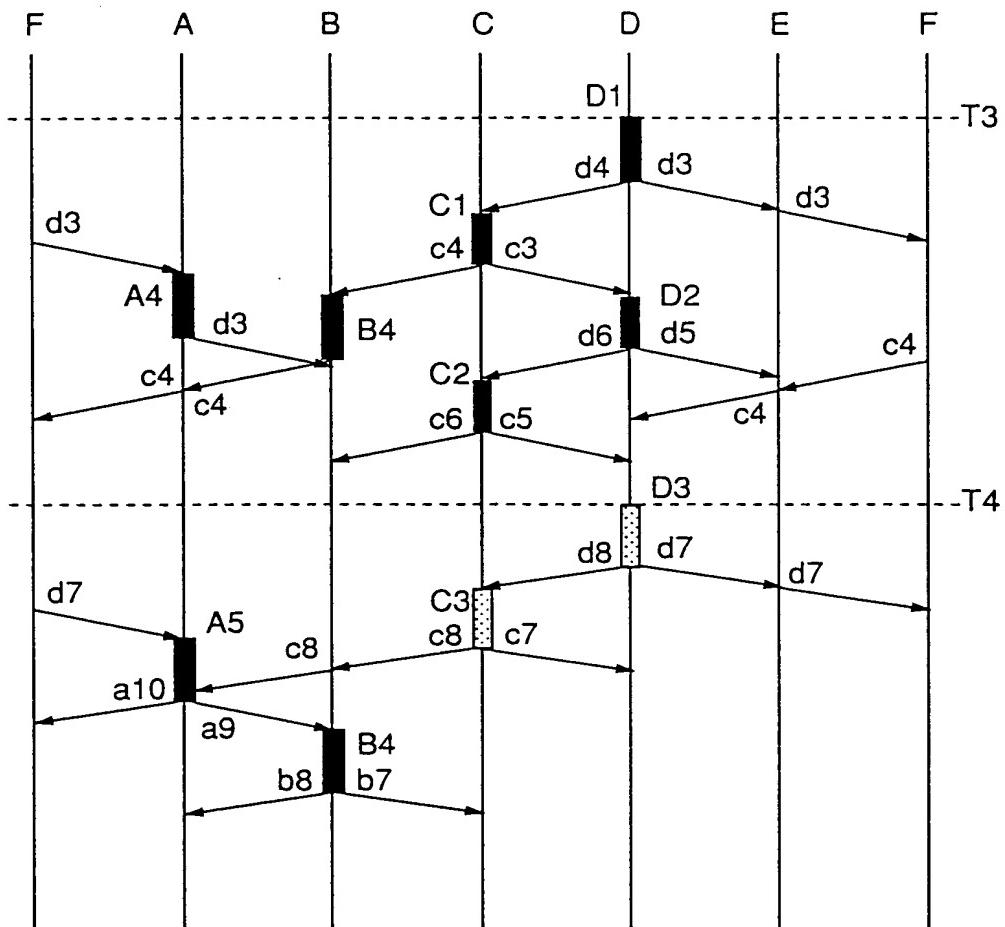


FIG. 9



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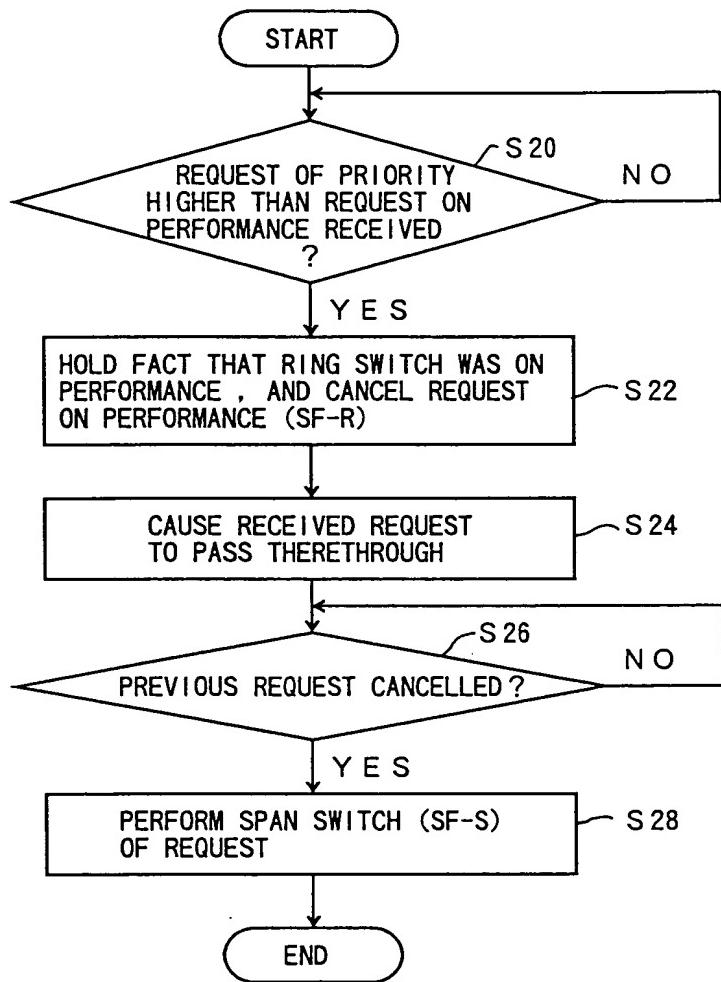
FIG. 10A

SYMBOL	K1 BYTE		K2 BYTE		
	BIT(1-4)	BIT(5-8)	BIT(1-4)	BIT(5)	BIT(6-8)
d 3	S F - S	C	D	LONG	IDLE
d 4	S F - S	C	D	SHORT	IDLE
c 3	R R - S	D	C	SHORT	Br
c 4	S F - S	D	C	LONG	Br
d 5	S F - S	C	D	LONG	Br&Sw
d 6	S F - S	C	D	SHORT	Br&Sw
c 5	R R - S	D	C	SHORT	Br&Sw
c 6	S F - S	D	C	LONG	Br&Sw
d 7	WTR	E	D	LONG	Br&Sw
d 8	WTR	C	D	SHORT	Br&Sw

FIG. 10B

c 7	R R - S	D	C	SHORT	Br&Sw
c 8	WTR	D	C	LONG	Br&Sw
a 9	S F - S	B	A	SHORT	IDLE
a 10	S F - S	B	A	LONG	IDLE
b 7	NR	C	B	SHORT	IDLE
b 8	NR	A	B	SHORT	IDLE

FIG. 11



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FIG. 12

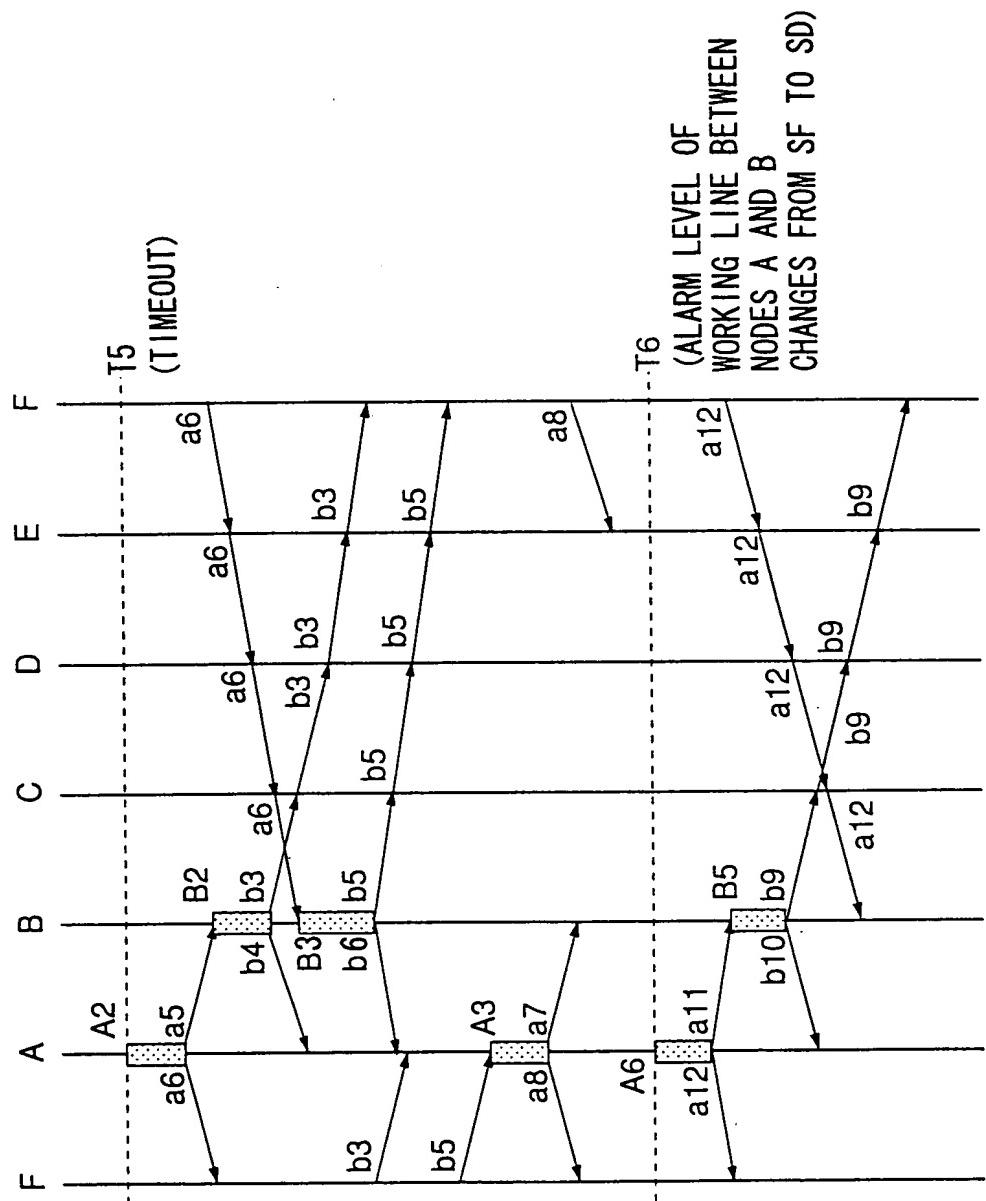
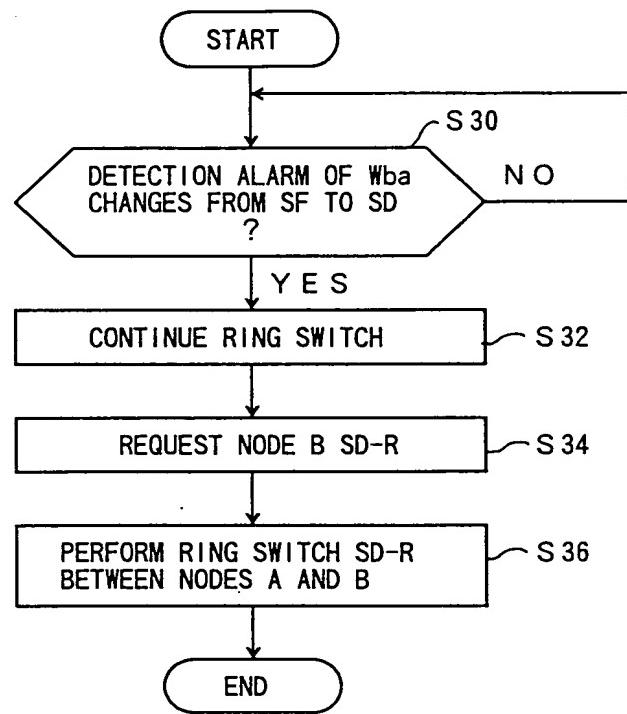


FIG. 13

SYMBOL	K1 BYTE		K2 BYTE		
	BIT(1-4)	BIT(5-8)	BIT(1-4)	BIT(5)	BIT(6-8)
a 11	S D - R	B	A	SHORT	Br&Sw
a 12	S D - R	B	A	LONG	Br&Sw
b 9	S D - R	A	B	SHORT	Br&Sw
b 10	R R - R	A	B	LONG	Br&Sw

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FIG. 14



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FIG. 15

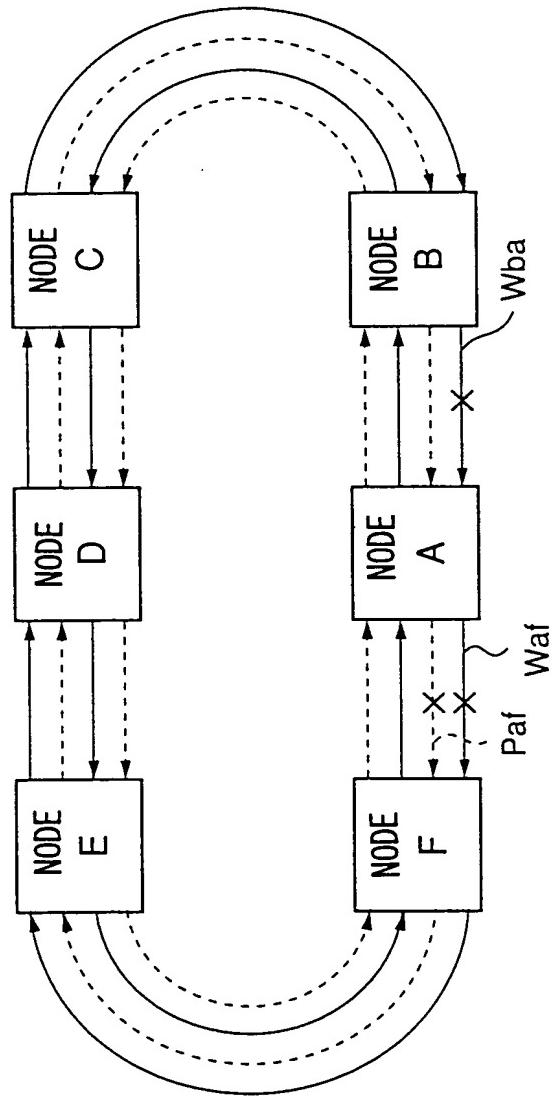
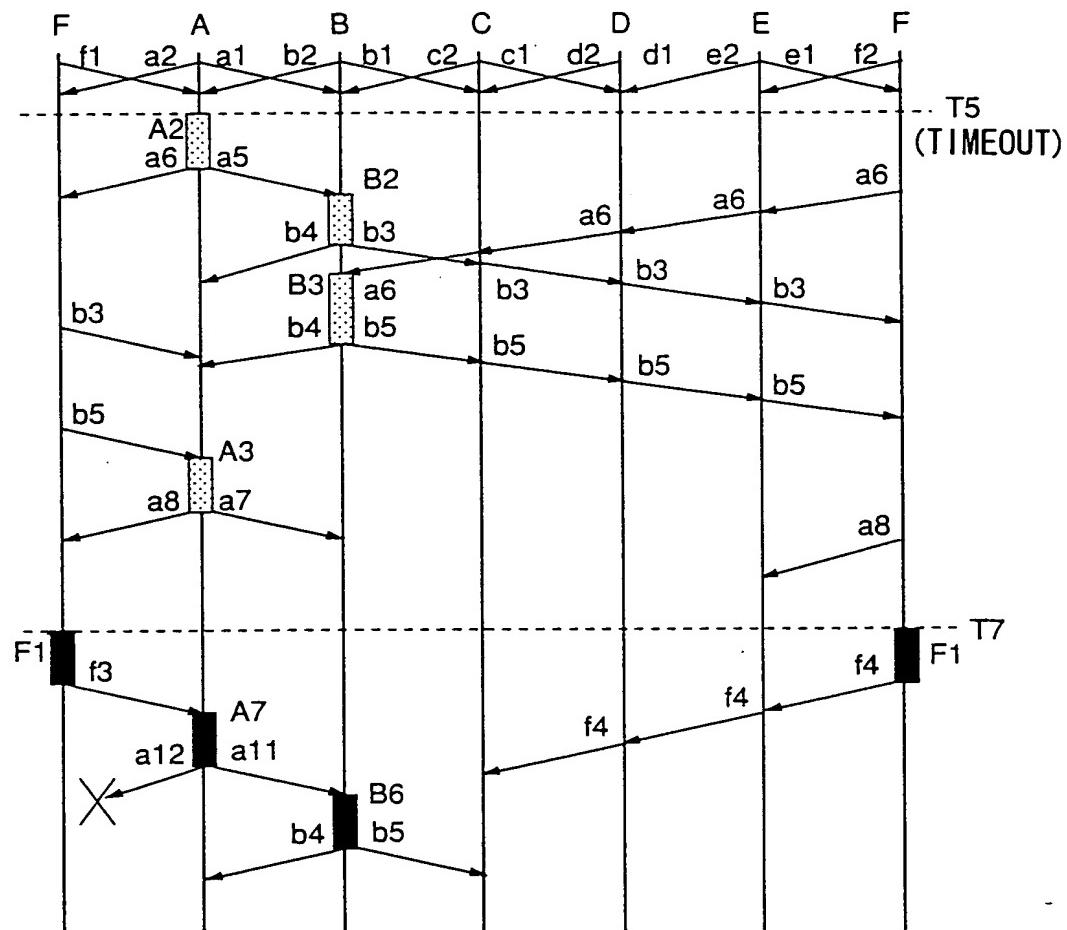


FIG. 16



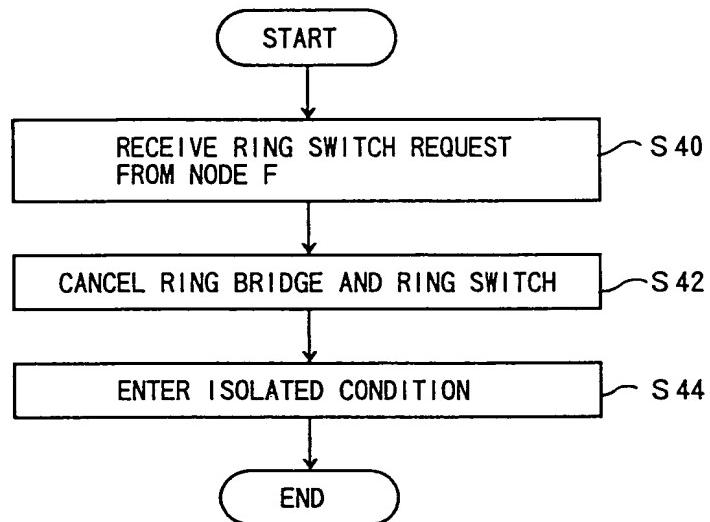
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FIG. 17

SYMBOL	K1 BYTE		K2 BYTE		
	BIT(1-4)	BIT(5-8)	BIT(1-4)	BIT(5)	BIT(6-8)
f 3	S F - R	A	F	SHORT	R D I
f 4	S F - R	A	F	LONG	B r & S w
a 11	S F - R	B	A	SHORT	IDLE
a 12	S F - R	B	A	LONG	IDLE

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FIG. 18



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FIG. 19

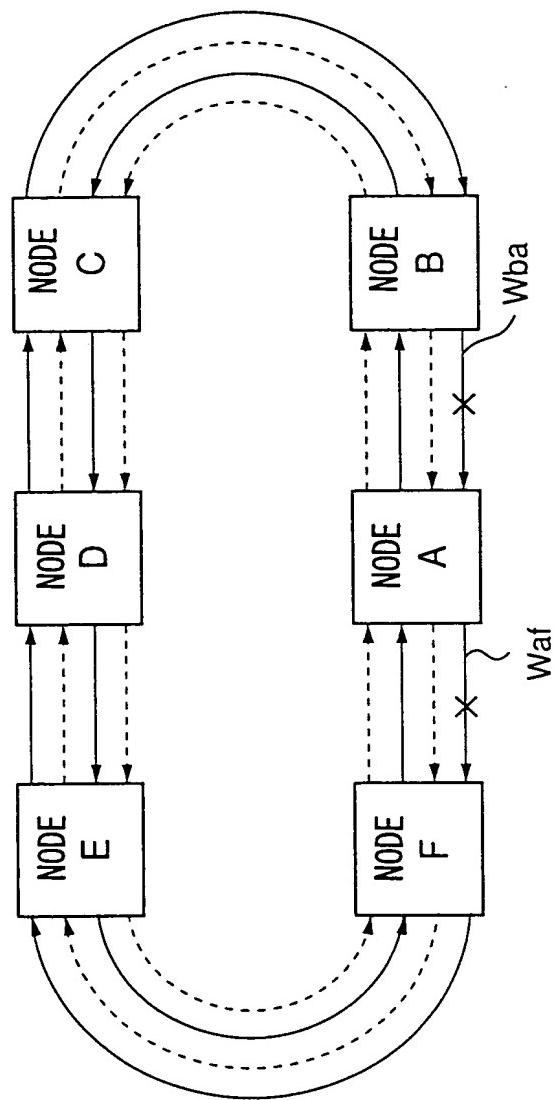
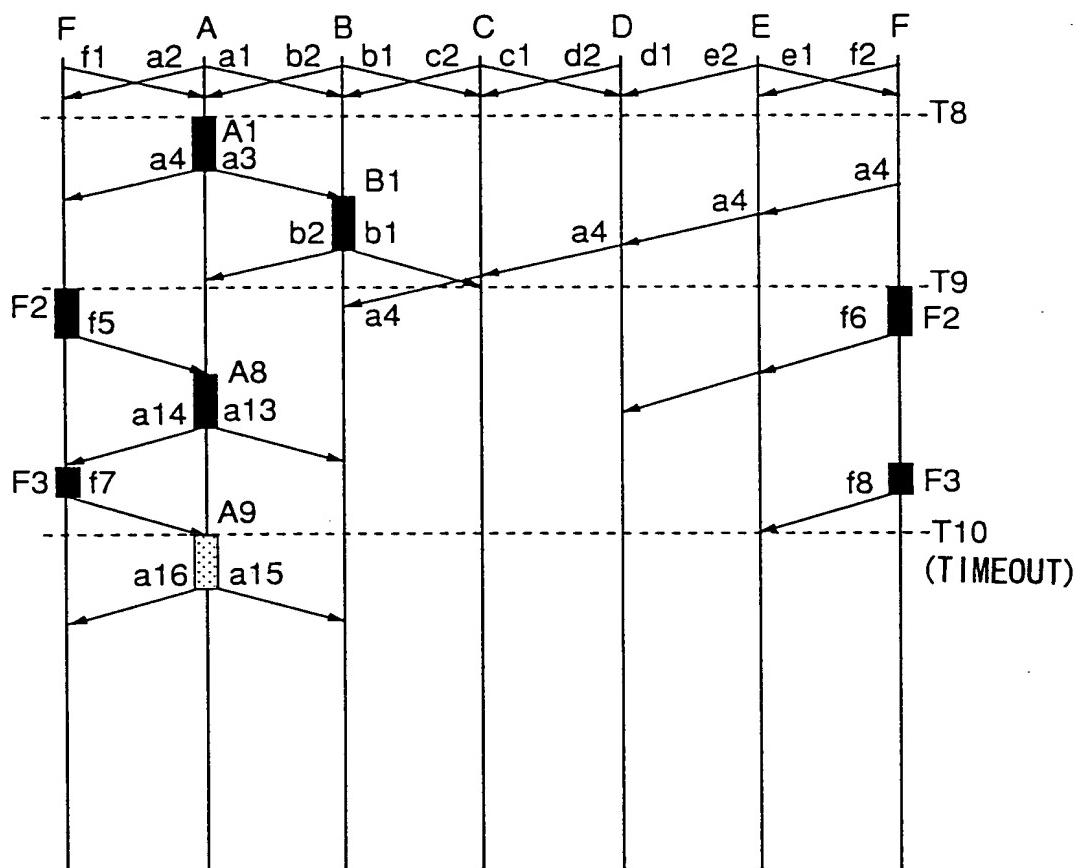


FIG. 20



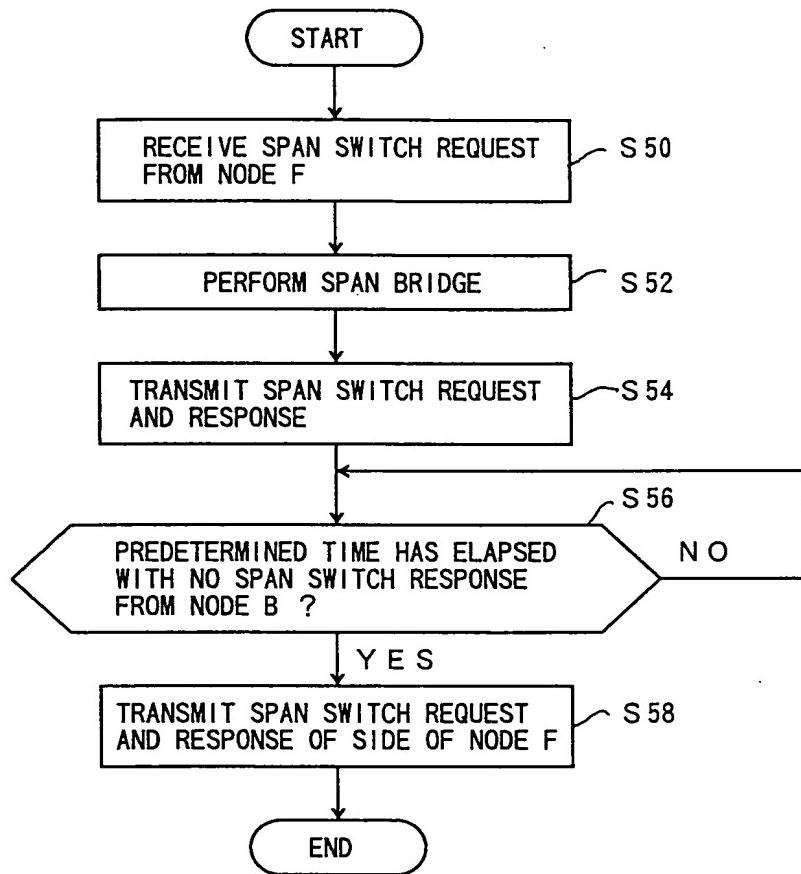
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FIG. 21

SYMBOL	K1 BYTE		K2 BYTE		
	BIT(1-4)	BIT(5-8)	BIT(1-4)	BIT(5)	BIT(6-8)
f 5	S F - S	A	F	SHORT	IDLE
f 6	S F - S	A	F	LONG	IDLE
a 13	S F - S	B	A	SHORT	IDLE
a 14	R R - S	F	A	SHORT	Br
f 7	S F - S	A	F	SHORT	Br&Sw
f 8	S F - S	A	F	LONG	Br&Sw
a 15	S F - S	F	A	LONG	Br&Sw
a 16	R R - S	F	A	SHORT	Br&Sw

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FIG. 22



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